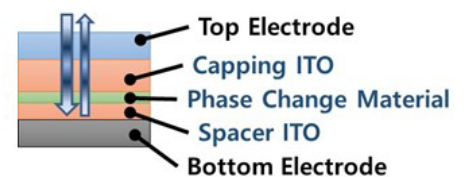
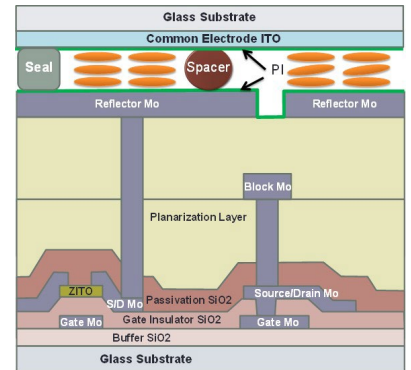
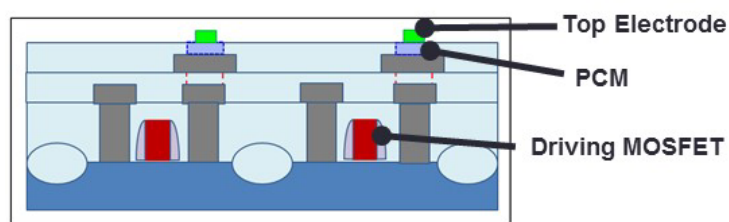
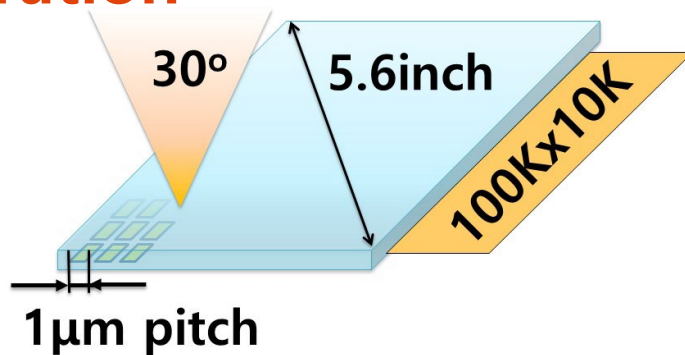


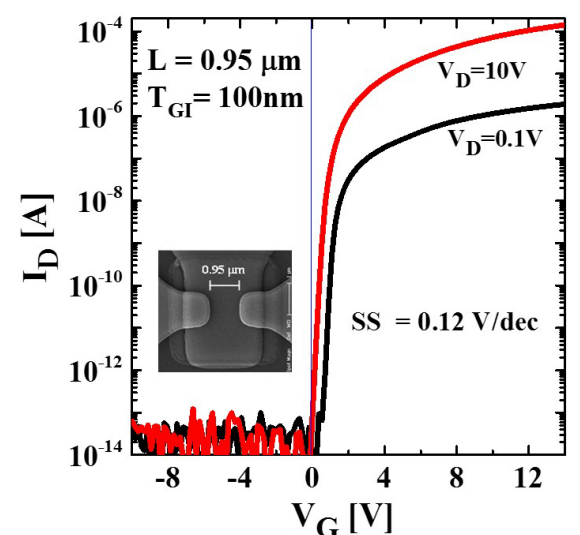
Overview & Configuration

- Ultra-high-resolution SLM panel and driving technology
- Device technology for SLM driving
- Optical modulation device technology for SLM (LC and PCM)



Key Features

Technical Feature	Conventional SLM for Holography (LCoS, DMD)	Flat Panel Display (LCD)	SLMoG (SLM on Glass) (Till 2021)
Min. Pixel Size	4~8μm	40~45μm (sub pixel : 13~15μm)	1μm
Screen Size	< 1"	5.5~13"	3"
Light Modulation	Phase, Intensity, Duration	Intensity, color	Phase
	Reflective	Transmissive	Reflective
Driving Device	CMOS	TFT (a-Si, poly-Si, oxide)	Oxide TFT
Substrate	Si wafer	Glass	Glass



Technological Competitiveness

- Development and operation of a 72K-level SLM panel with 1μm pitch pixels
- Ultra-small oxide TFT device technology with sub-μm channel lengths
- Optical modulation device technology with no intercell interference at 1μm pixel pitch

Application Products & Fields

- Digital hologram displays
- Ultra-high-resolution displays for AR/VR
- Optical modulation applications, such as adaptive optics using ultra-high-resolution SLMs